

Abstracts

Modeling source inductance in FETs

D. Farias, F. Rose and J.R. Selin. "Modeling source inductance in FETs." 2000 MTT-S International Microwave Symposium Digest 00.3 (2000 Vol. III [MWSYM]): 1793-1796.

An analytical model for the calculation of FET source inductance is presented. This model is based on equations for self and mutual inductance of straight conductors and cylindrical via holes derived from Maxwell's equations. It provides designers with a tool to analytically scale source inductance for different FET structures.

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